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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/770,148	01/26/2001	Gary Douglas Huber	. M-9876US	7341
27683 75	590 06/02/2005		EXAM	INER
HAYNES AND BOONE, LLP 901 MAIN STREET, SUITE 3100			MARTINEZ	, DAVID E
DALLAS, TX 75202			ART UNIT	PAPER NUMBER -
			2182	
			DATE MAILED: 06/02/2009	ξ

Please find below and/or attached an Office communication concerning this application or proceeding.

					
	Application No.	Applicant(s)			
065 - 4 - 45 0	09/770,148	HUBER ET AL.			
Office Action Summary	Examiner	Art Unit			
	David E. Martinez	2182			
The MAILING DATE of this communication appears on the cover sheet with the correspondence address Period for Reply					
A SHORTENED STATUTORY PERIOD FOR REPL THE MAILING DATE OF THIS COMMUNICATION. - Extensions of time may be available under the provisions of 37 CFR 1. after SIX (6) MONTHS from the mailing date of this communication. - If the period for reply specified above is less than thirty (30) days, a replection of the period for reply is specified above, the maximum statutory period. - Failure to reply within the set or extended period for reply will, by statut Any reply received by the Office later than three months after the mailing earned patent term adjustment. See 37 CFR 1.704(b).		ireply be timely filed irty (30) days will be considered timely. NTHS from the mailing date of this communication. ABANDONED (35 U.S.C. § 133).			
Status					
1) Responsive to communication(s) filed on 18 A	April 2005.				
2a)⊠ This action is FINAL . 2b)□ Thi	This action is FINAL . 2b) ☐ This action is non-final.				
Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under <i>Ex parte Quayle</i> , 1935 C.D. 11, 453 O.G. 213.					
Disposition of Claims					
 4) Claim(s) 1-15 and 17 is/are pending in the application. 4a) Of the above claim(s) is/are withdrawn from consideration. 5) Claim(s) is/are allowed. 6) Claim(s) 1-15 and 17 is/are rejected. 7) Claim(s) is/are objected to. 8) Claim(s) are subject to restriction and/or election requirement. 					
Application Papers					
9) The specification is objected to by the Examine 10) The drawing(s) filed on <u>04 October 2002</u> is/are Applicant may not request that any objection to the Replacement drawing sheet(s) including the correct 11) The oath or declaration is objected to by the E	e: a) accepted or b) cetter and accepted or b) cetter accepted in abeyant ction is required if the drawing	nce. See 37 CFR 1.85(a). g(s) is objected to. See 37 CFR 1.121(d).			
Priority under 35 U.S.C. § 119		•			
12) Acknowledgment is made of a claim for foreign a) All b) Some * c) None of: 1. Certified copies of the priority document 2. Certified copies of the priority document 3. Copies of the certified copies of the priority application from the International Bureat * See the attached detailed Office action for a list	ts have been received. ts have been received in a prity documents have been tu (PCT Rule 17.2(a)).	Application No n received in this National Stage			
Attachment(s) 1) Notice of References Cited (PTO-892) 2) Notice of Draftsperson's Patent Drawing Review (PTO-948) 3) Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08) Paper No(s)/Mail Date	Paper No	Summary (PTO-413) (s)/Mail Date Informal Patent Application (PTO-152)			

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DETAILED ACTION

Claim Rejections - 35 USC § 103

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.

Claims 1-9, and 17 are rejected under 35 U.S.C. 103(a) as being unpatentable over Applicant's Admitted Prior Art (AAPA) in view of US Patent No. 6,038,670 to Oh, and further in view of US Patent No. 6,493,782 to Verdun et al. (Verdun).

- 1. With regards to claims 1 and 17, AAPA teaches a mobile computing system comprising of:
 - a personal computer (PC) system [fig 1];
- a personal digital assistant (PDA) system [fig 2] that interfaces to the PC system [page 3 lines 13-20];
 - a PC chassis [page 3, lines 21-24];
- a PDA chassis housing the PDA system [fig 2] wherein the PC chassis hosts the PC system and the PDA chassis, whereby the PDA chassis may be removed from the PC chassis while in any mode, disconnecting an interface of the PDA system [fig 2 element 220] to the PC system, and providing an independent PDA system [figs 1, 2, page 3, lines 3-24], the PDA automatically transitioning to an independent power source and reconfiguring its input devices; and

the PDA chassis including:

a memory [fig 2, page 3 lines 3-12], and

an independent source of power [fig 2, page 3 lines 3-12].

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AAPA teaches all of the above limitations except for the PDA chassis including a processor connected to a system co-processor which controls I/O communications, an I/O device interfacing with the co-processor by means of an I/O bus, an independent source of power connected to the co-processor, a first video bus connecting the PC and the PDA to a common display; and a second video bus connecting a video controller to a PDA display, whereby the second video bus is inactive when the PDA is coupled to the PC. AAPA is also silent to the PDA chassis being able to be removed from the PC chassis while in any mode, and the PDA automatically transitioning to an independent power source and reconfiguring its input devices.

However, Oh teaches a processor [fig 1 element 11] connected to a system coprocessor [fig 1 element 12] which controls I/O communications [column 3 lines 16-25];

an I/O device [fig 1 elements 13, 11, and 14] interfacing with the co-processor [fig 1 element 12] by means of an I/O bus [fig 1, bus lines connecting elements 11, 12, 13, 14];

an independent source of power connected to the co-processor [although not shown on fig 1, there must be an independent source of power to provide the computing elements with energy to operate, see figs 4 and 5, "power supply" elements". In addition, AAPA already supplies an independent source of power as disclosed above];

a first video bus [fig 1, "image signal" bus line] connecting the PC [fig 1 element 16, although is a CRT, PCs are well known in the art to have video in, and thus connecting a portable device to either a CRT or a PC is the same if the particular PC has "TV/Video in" feature] and the PDA [fig 1 element 10] to a common display [fig 1 element 16]; and

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a second video bus [fig 1 bus line running from element 12 to element 13] connecting a video controller [fig 1 element 12] to a PDA display [fig 1 element 13], whereby the second video bus is inactive when the PDA is coupled to the PC [see abstract] for the benefit of sharing video with an external device while at the same time preventing unnecessary power consumption by the internal display (LCD) of the portable device and thus saving energy and providing extended battery life.

Furthermore, Verdun teaches a portable computer (PDA chassis) being able to be removed from a docking unit (PC chassis) while in any mode, and the PDA automatically transitioning to an independent power source and reconfiguring its input devices [column 2 lines 38-57 and line 66 to column 3 line 5] for the benefit of being able to connect and disconnect the portable computer in all power state configurations.

It would have been obvious to one of ordinary skill in the art at the time of the invention to combine the teachings of AAPA, Oh and Verdun to provide a processor connected to a system co-processor which controls I/O communications, an I/O device interfacing with the co-processor by means of an I/O bus, an independent source of power connected to the co-processor, a first video bus connecting the PC and the PDA to a common display, and a second video bus connecting a video controller to a PDA display, whereby the second video bus is inactive when the PDA is coupled to the PC, and to have the PDA chassis be able to be removed from the PC chassis while in any mode, and the PDA automatically transitioning to an independent power source and reconfiguring its input devices for the benefits of sharing video with an external device while at the same time preventing unnecessary power consumption by the internal display (lcd) of the portable device and thus saving energy and providing extended battery life and for being able to connect and disconnect the portable computer in all power state configurations.

2. With regards to claim 2, AAPA teaches the mobile computing system of claim 1 wherein the PDA system is further comprised of:

an input device [fig 2, elements 205, 210, page 3 lines 3-12].

- 3. With regards to claim 3, AAPA teaches the mobile computing system of claim 2 further comprising: an antenna for wireless communications [fig 2, element 215, page 3 lines 3-12].
- 4. With regards to claims 4, 5, and 6, AAPA teaches wherein the PC chassis further comprises one or more expansion bays [fig 1, elements 110, 115], wherein the PDA chassis is placed in one of the bays [page 2 lines 16-21, page 3, lines 13-22].
- 5. With regards to claims 7, 8, and 9, AAPA teaches wherein the PDA chassis is placed in the interior of the PC chassis [fig 1, elements 110, 115, page 2 lines 16-21, page 3, lines 13-22].
- 6. With further regards to claim 17, AAPA teaches a method of integrating a removable PDA system [fig 2] with a PC system [fig 1] comprised of:

connecting the PDA system to the PC system by a separable interface [page 3 lines 3-24];

isolating control to either PDA system or PC system when instructed by a user or a predetermined system logic [page 3, lines 13-22].

Claims 10-15 are rejected under 35 U.S.C. 103(a) as being unpatentable over Applicant's Admitted Prior Art (AAPA) in view of US Patent No. 6,038,670 to Oh. as applied to claim 1-3 above, and further in view of US Patent No. 5,768,163 to Smith, II (Smith).

7. With regards to claims 10-15, the combination of AAPA and Oh fail to teach wherein the PDA chassis is placed on the exterior of the PC chassis, and on the top of the PC chassis.

However, Smith teaches the use of a connector for connecting a PDA with a PC chassis [figs 1-7, 10, column 1 line 40 to column 2 line 34] for the benefit increasing the ease of sharing of information between devices.

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It would have been obvious to one of ordinary skill in the art at the time of the invention to combine the teachings of AAPA, Oh, and Smith to provide the PDA chassis is able to be placed on both the exterior and on top of the PC chassis for the benefit of increasing the ease of sharing of information between devices.

Response to Arguments

Applicant's arguments filed on 4/18/05 with respect to claims 1-15 and 17, have been considered but are most in view of the new ground(s) of rejection.

Applicant argues the previously mailed Office Action dated 8/24/04 does not disclose the newly added limitations of amended claims 1 and 17 directed to the PDA chassis. The Applicant only argues generic terms and does not argue (doesn't even mention) any details of the references relied upon by the examiner. Applicant only argues the amended claim and does not mention any details directed to the previous office action. The current Office Action discloses the newly added limitations directed to the PDA chassis as shown above in the "Claim Rejection" section.

Conclusion

The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.

US Patent No. 5,648,762 to Ichimura et al.

US Patent No. 5,052,943 to Davis.

US Patent No. 6,609,207 to Cromer et al.

US Patent No. 6,043,626 to Snyder et al.

Applicant's amendment necessitated the new ground(s) of rejection presented in this Office action. Accordingly, **THIS ACTION IS MADE FINAL**. See MPEP § 706.07(a). Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the date of this final action.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to David E Martinez whose telephone number is (571) 273-4152. The examiner can normally be reached on 8:30-5:00 M-F.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Jeffrey A Gaffin can be reached on (571) 272-4146. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

DEM

PRIMARY EXAMINER
GROUP 2100